S850		USPT	5218723.pn.	☐
S849		USPT	5218722.pn.	☐
S848		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(enabl$ with (write adj$3 access) with (power adj$2 reset) with (security)) and (@adL5 and (flag bit indicator)	☐
S847		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(enabl$ with (write adj$3 access) with (power adj$2 reset) with (security)) and (@ad<=2002 @py<=2002)	☐
S846		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(enabl$ with (write adj$3 access) with (power adj$2 reset) with (security)) AND @pd>20050808	☐
S845		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enabl$ with (write adj$3 access) with (power adj$2 reset) with (security)	☐
S844		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enabl$ with (write adj$3 access) with (power adj$2 reset)	☐
S843		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enabl$ with (write adj$3 access)	☐
S842		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write with ("power-on reset") with (control$ access$ allow$) and (@ad<=2002 @py<=2002)	☐
S841		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write with ("power-on reset") with (control$ access$ allow$)	☐
S840		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write with ("power-on reset")	☐
S839		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write	☐
S838		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with (power adj on adj reset) with security	☐

S810		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl$) with (write$) and (@adL4 and (secur$)	☐
S809		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl$) with (write$) and (@ad<=2002 @py<=2002)	☐
S808		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl$) with (write$)	☐
S807		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl$)	